

**ABSTRACT OF THE DISCLOSURE**

A method and circuit are disclosed for detecting the performance of an oscillator circuit. In particular, the circuit may detect a signal, such as the output of the oscillator circuit, failing to oscillate as desired. The  
5 second circuit may be capable of detecting whether the signal oscillates at a frequency that is less than a predetermined frequency. The second circuit may include timing circuits for determining whether the signal remains in a first logic  
10 state for at least a predetermined period of time and whether the signal remains in a second logic state for at least the predetermined period of time.